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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: MIYAZAKI, et al
Serial No.: Not Yet Assigned
Filed: On Even Date
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.97 & 1.98

Assistant Commissioner for Patents
Washington, D.C. 20231

March 20, 2001

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of the document listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

Each of the documents listed on the attached form equivalent to form PTO-1449 is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time

fees, to the deposit account of Antonelli, Terry, Stout &
Kraus Deposit Account No. 01-2135 (Case: 500.39910X00), and
please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS LLP



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Attachments